



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
-----------------	-------------	----------------------	---------------------	------------------

10/730,380

12/09/2003

Hiroichi Makida

925-277

2848

23117

7590

05/19/2006

NIXON & VANDERHYE, PC
901 NORTH GLEBE ROAD, 11TH FLOOR
ARLINGTON, VA 22203

EXAMINER

WILSON, YOLANDA L

ART UNIT

PAPER NUMBER

2113

DATE MAILED: 05/19/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/730,380

Applicant(s)

MAKIDA ET AL.

Examiner

Yolanda L. Wilson

Art Unit

2113

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 09 December 2003.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-18 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 14, 15, 17 and 18 is/are allowed.
- 6) ☒ Claim(s) 1, 5, 9, 13 and 16 is/are rejected.
- 7) ☒ Claim(s) 2-4, 6-8 and 10-12 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 12/9/03;09/13/05.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Claim Objections

1. Claims 2-4,6-8,10-12 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
2. Claim 5 is objected to because of the following informalities: in claim 5, line 25, 'debag' should be 'debug'. Appropriate correction is required.

Allowable Subject Matter

3. Claims 14,15,17,18 are allowed.
4. The following is a statement of reasons for the indication of allowable subject matter: Claim 14 and 17 are allowed because of the inclusion of the following limitations: storing debug data indicating a corresponding relation between the parallel program and the serial program in storage means. Claim 15 and 18 are allowed because of the inclusion of the following limitations: creating debug data indicating a corresponding relation between the parallel program and the serial program; storing the debug data in storage means.

Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Art Unit: 2113

6. Claims 1,5,9,13,16 are rejected under 35 U.S.C. 102(b) as being anticipated by Bernstein et al. (EP 0406602A2). As per claim 1, Bernstein et al. discloses a debug device for use in converting a parallel program to a serial program and executing debugging in column 6, lines 13-24, comprising: serialization means for converting the parallel program to the serial program and creating debug data that indicates a corresponding relation between the parallel program and the serial program in column 11, lines 18-24,30-34,46-55; column 12, lines 21-25; and storage means for storing the debug data in column 12, lines 37-40.

7. As per claim 5, Bernstein et al. discloses a debug device for use in converting a parallel program to a serial program and executing debugging in column 6, lines 13-24, comprising: storage means for storing debug data that indicates a corresponding relation between the parallel program and the serial program; and conversion means for mutually converting corresponding data between the parallel program and the serial program based on the debug data in column 11, lines 18-24,30-34,46-55; column 12, lines 21-25; column 12, lines 37-40. The conversion means is performed by the queries entered for the program by the user as discloses in columns 11 and 12.

8. As per claim 9, Bernstein et al. discloses a debug device for use in converting a parallel program to a serial program and executing debugging in column 6, lines 13-24, comprising: serialization means for converting the parallel program to the serial program and creating debug data that indicates a corresponding relation between the parallel program and the serial program in column 11, lines 18-24,30-34,46-55; column 12, lines 21-25; storage means for storing the debug data in column 12, lines 37-40; and

Art Unit: 2113

conversion means for mutually converting corresponding data between the parallel program and the serial program based on the debug data in column 11, lines 18-24,30-34,46-55; column 12, lines 21-25. The conversion means is performed by the queries entered for the program by the user as discloses in columns 11 and 12.

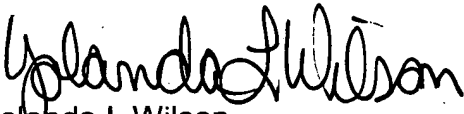
9. As per claims 13 and 16, Bernstein discloses a debug method for converting a parallel program to a serial program and executing debugging in column 6, lines 13-24, comprising: converting the parallel program to the serial program by serialization means in column 11, lines 18-24,30-34,46-55; column 12, lines 21-25; creating debug data indicating a corresponding relation between the parallel program and the serial program in column 11, lines 18-24,30-34,46-55; column 12, lines 21-25; and storing the debug data in storage means in column 12, lines 37-40. The conversion means is performed by the queries entered for the program by the user as discloses in columns 11 and 12.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yolanda L. Wilson whose telephone number is (571) 272-3653. The examiner can normally be reached on M-F (7:30-4:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Beausoliel can be reached on (571) 272-3645. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2113

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Yolanda L Wilson
Examiner
Art Unit 2113